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Application Number	10/749,618
Filing Date	December 30, 2003
First Named Inventor:	Steven K. Reinhardt
Group Art Unit	2184 2183
Examiner Name	William Partridge

Attorney Docket Number 42P17412

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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OTHER ART, NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
WBP		European Patent Office, International Search Report and Written Opinion for International Application No. PCT/US2004/043685, 15 pages, 23 May 2005.	
WBP		RO滕BERG, ERIC, "AR-SMT: A Microarchitectural Approach to Fault Tolerance in Microprocessors," Proceedings of the 29 th Annual International Symposium on Fault-Tolerant Computing, IEEE Computer Society, pp. 84-91, 15 June 1999.	
WBP		REINHARDT, STEVEN K. et al., "Transient Fault Detection via Simultaneous Multithreading," Computer Architecture News, Vol. 28, No. 2, pp. 25-36, May 2000.	

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